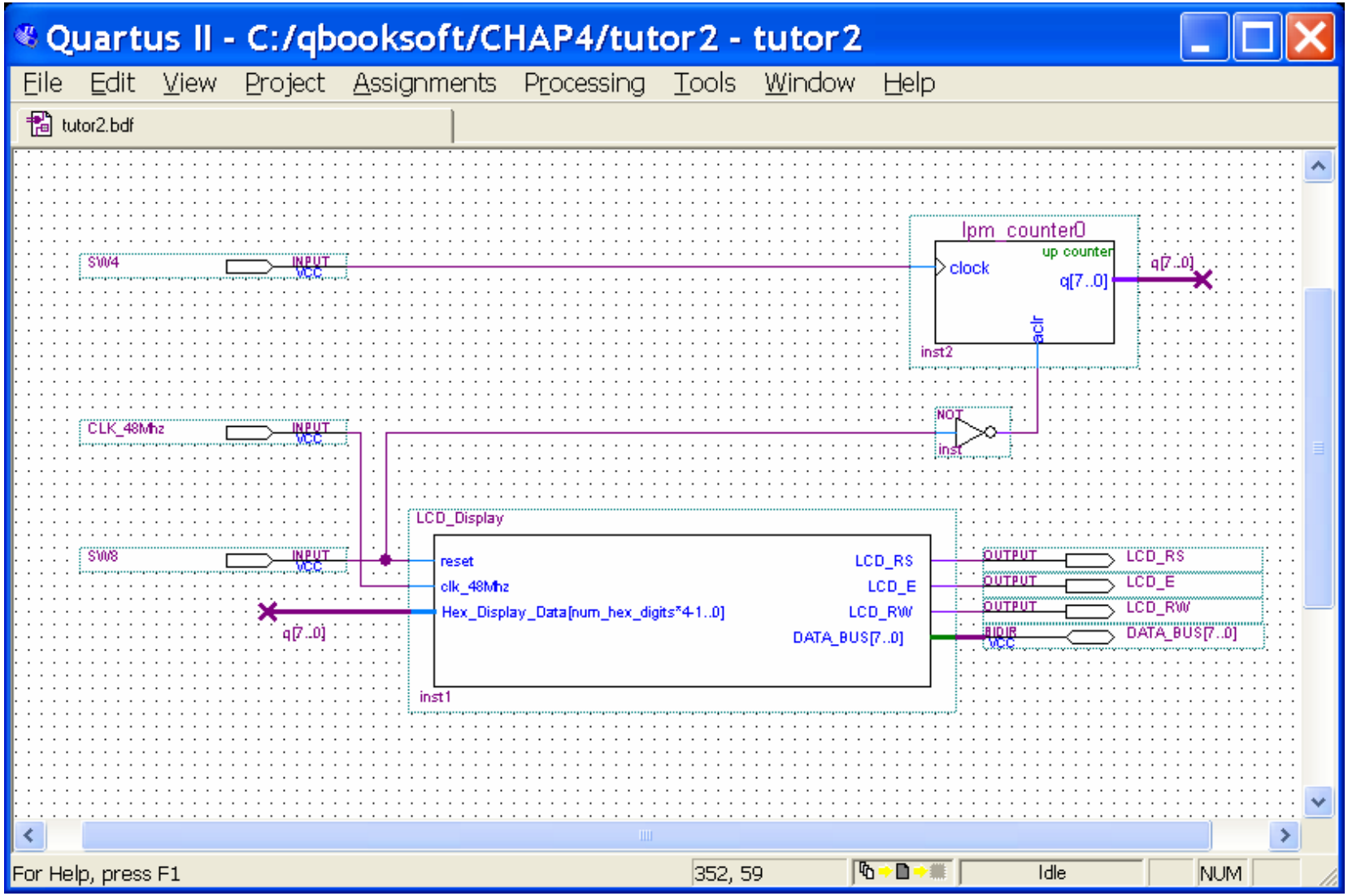

Figure 4.1 The tutor2.gdf schematic.

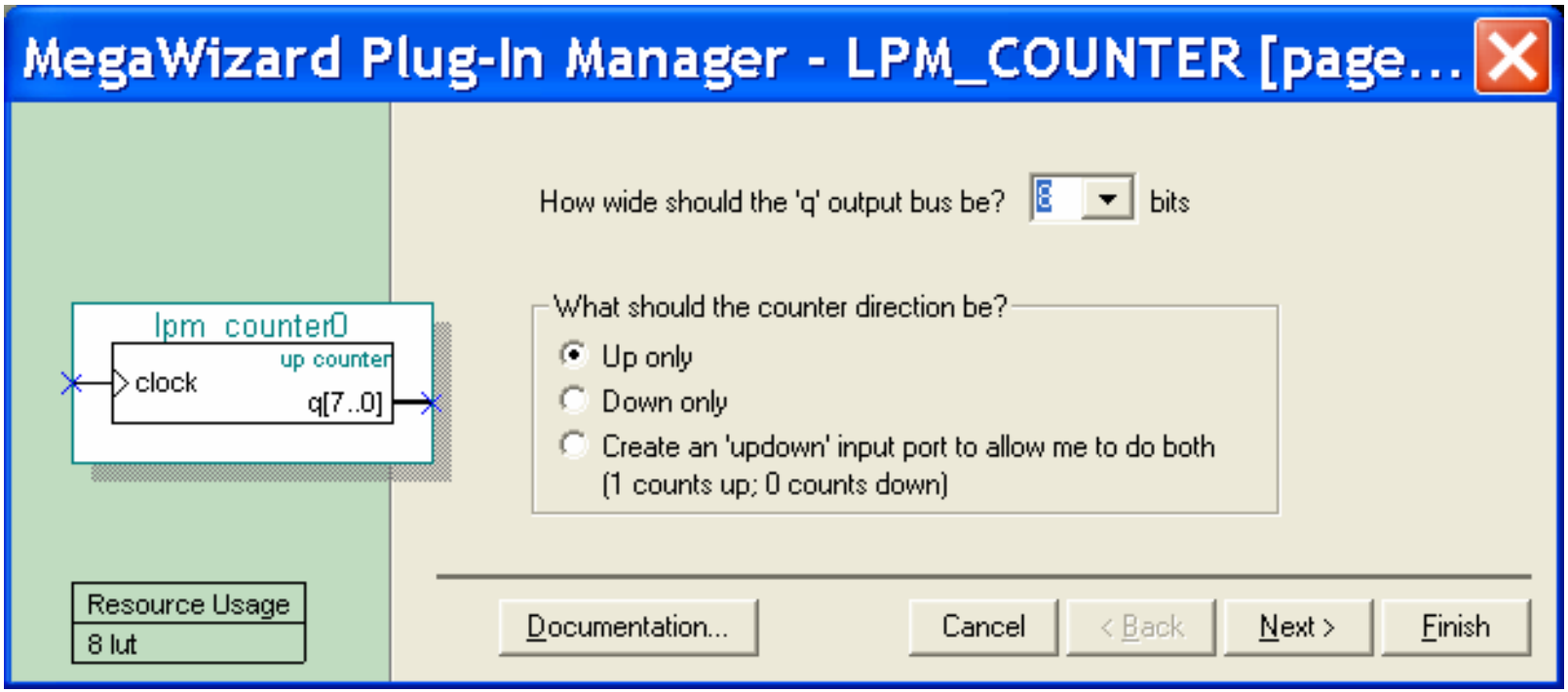

Figure 4.2 Lpm_counter0 MegaWizard edit window.

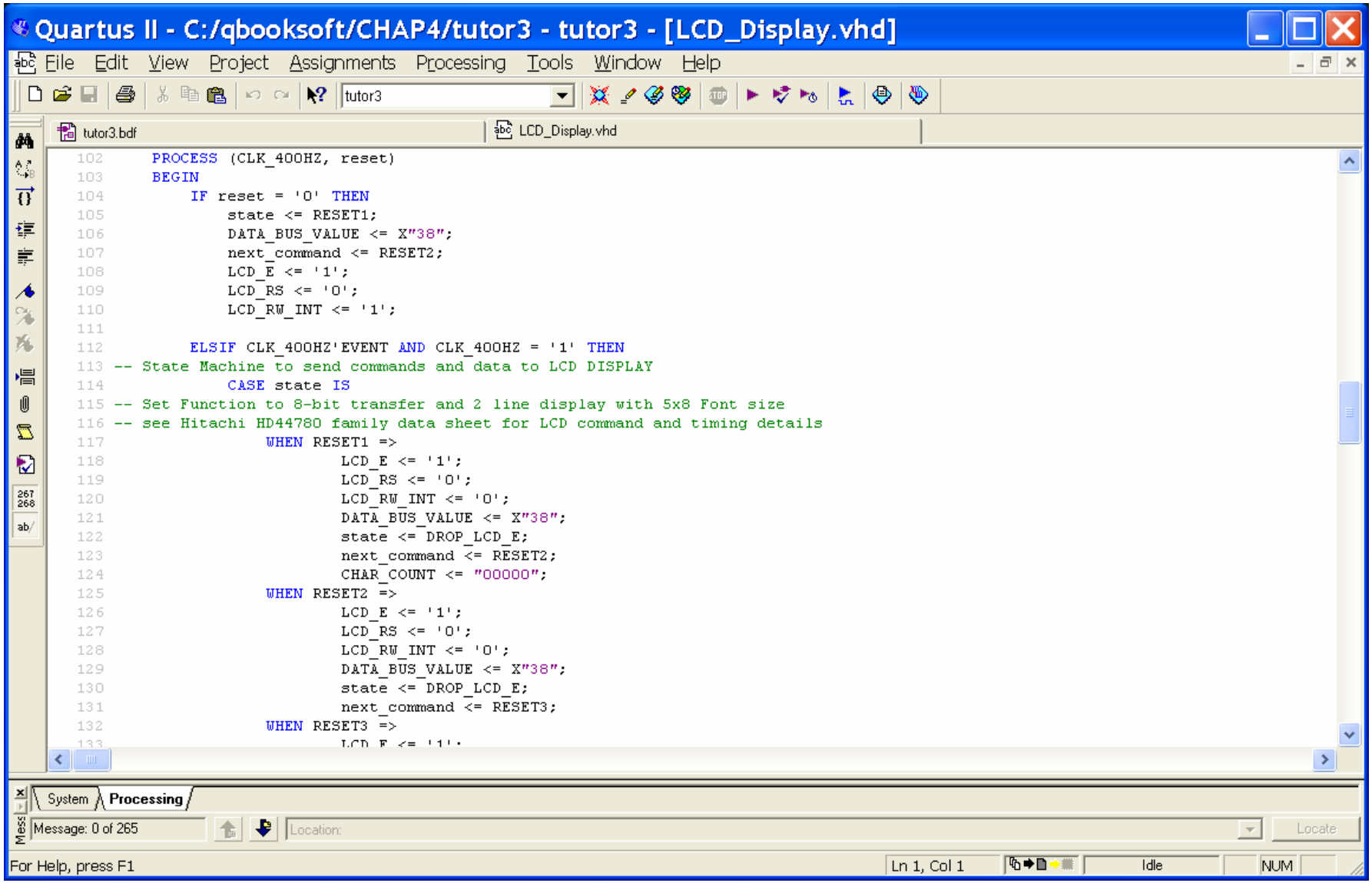

Figure 4.3 Internal VHDL code for LCD_Display function.

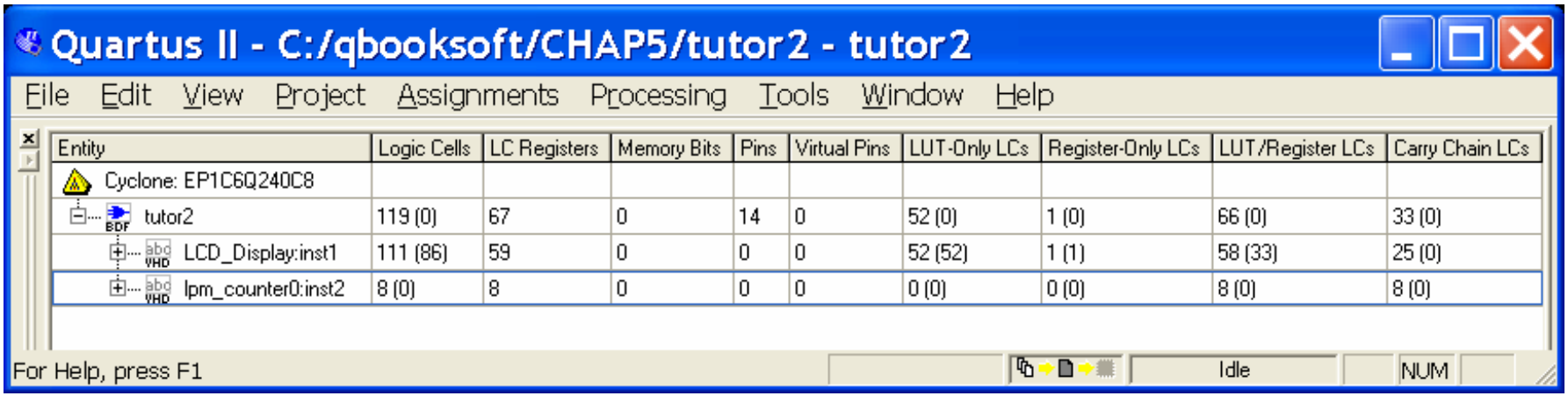

Figure 4.4 Hierarchy display of the tutor2 design.

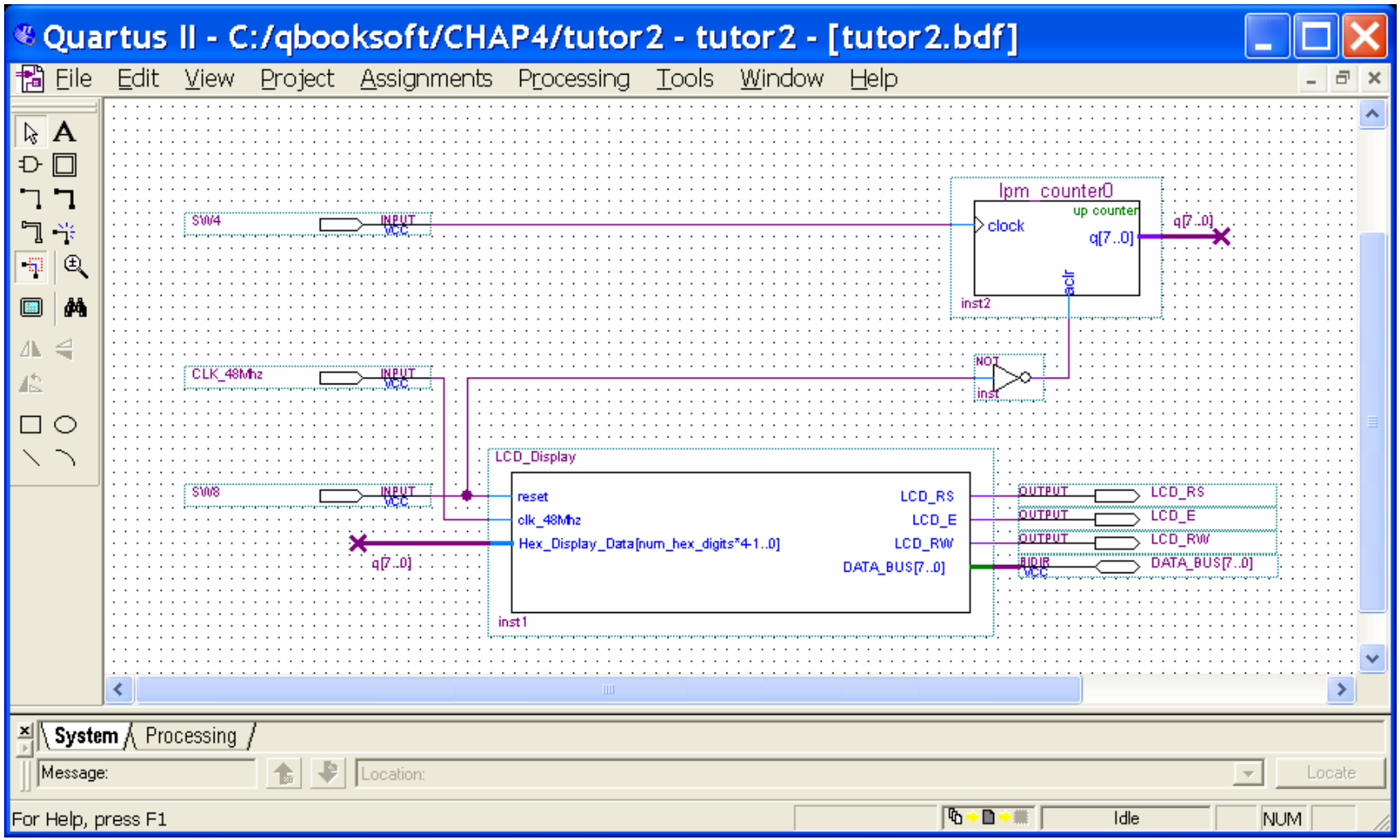

Figure 4.5 Enlarged view of tutor2 design showing bus connections.

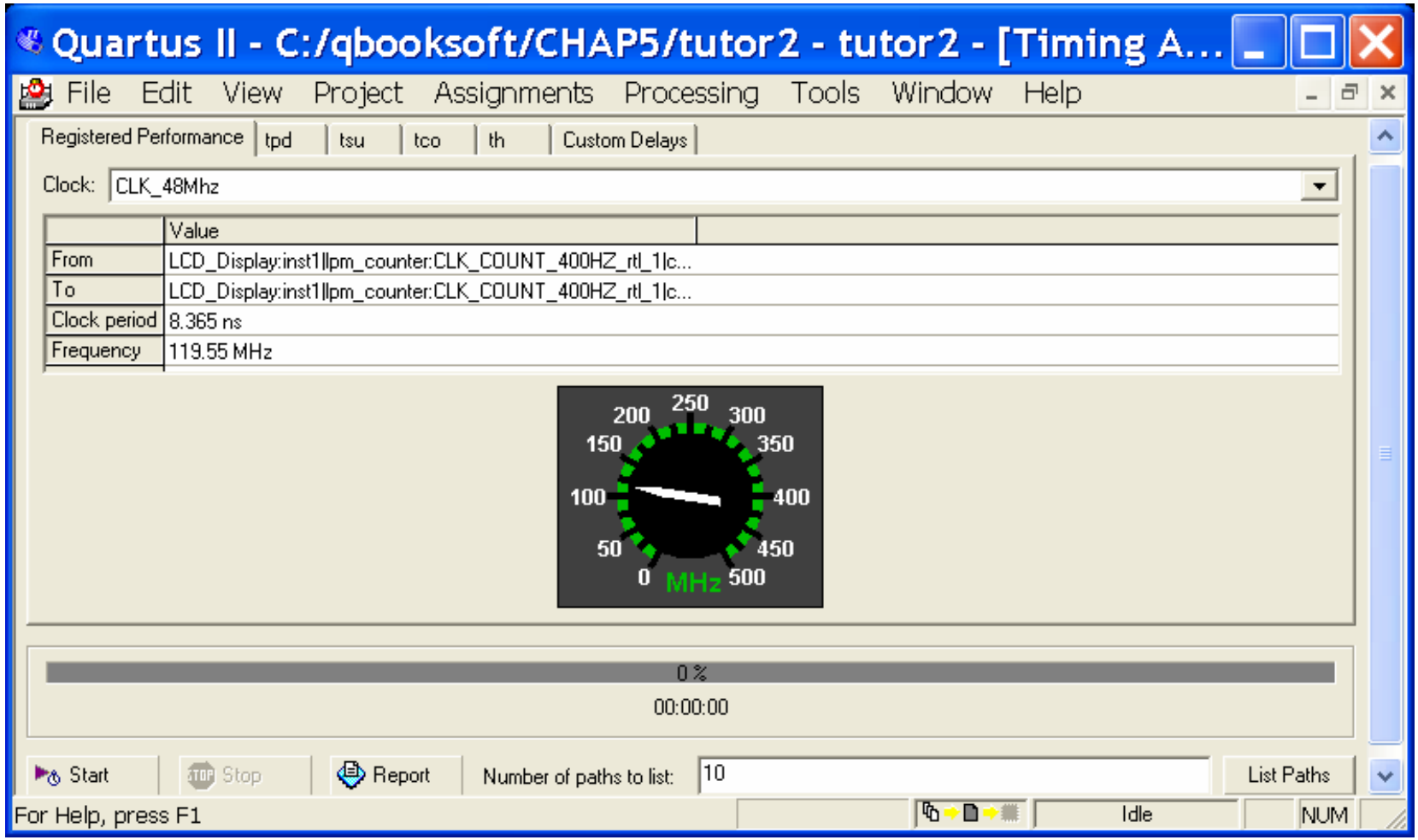

Figure 4.6 Timing analysis of a Sequential Circuit.

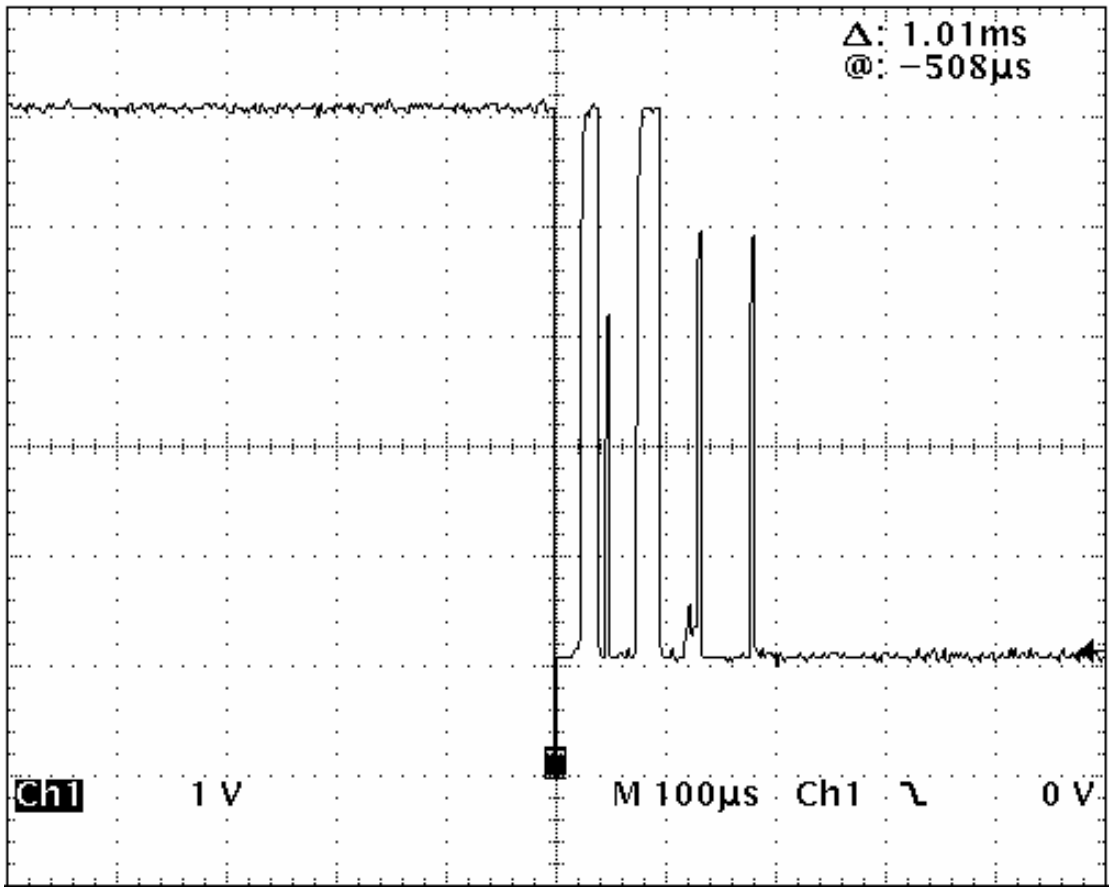

Figure 4.7 Oscilloscope display of pushbutton switch contact bounce.

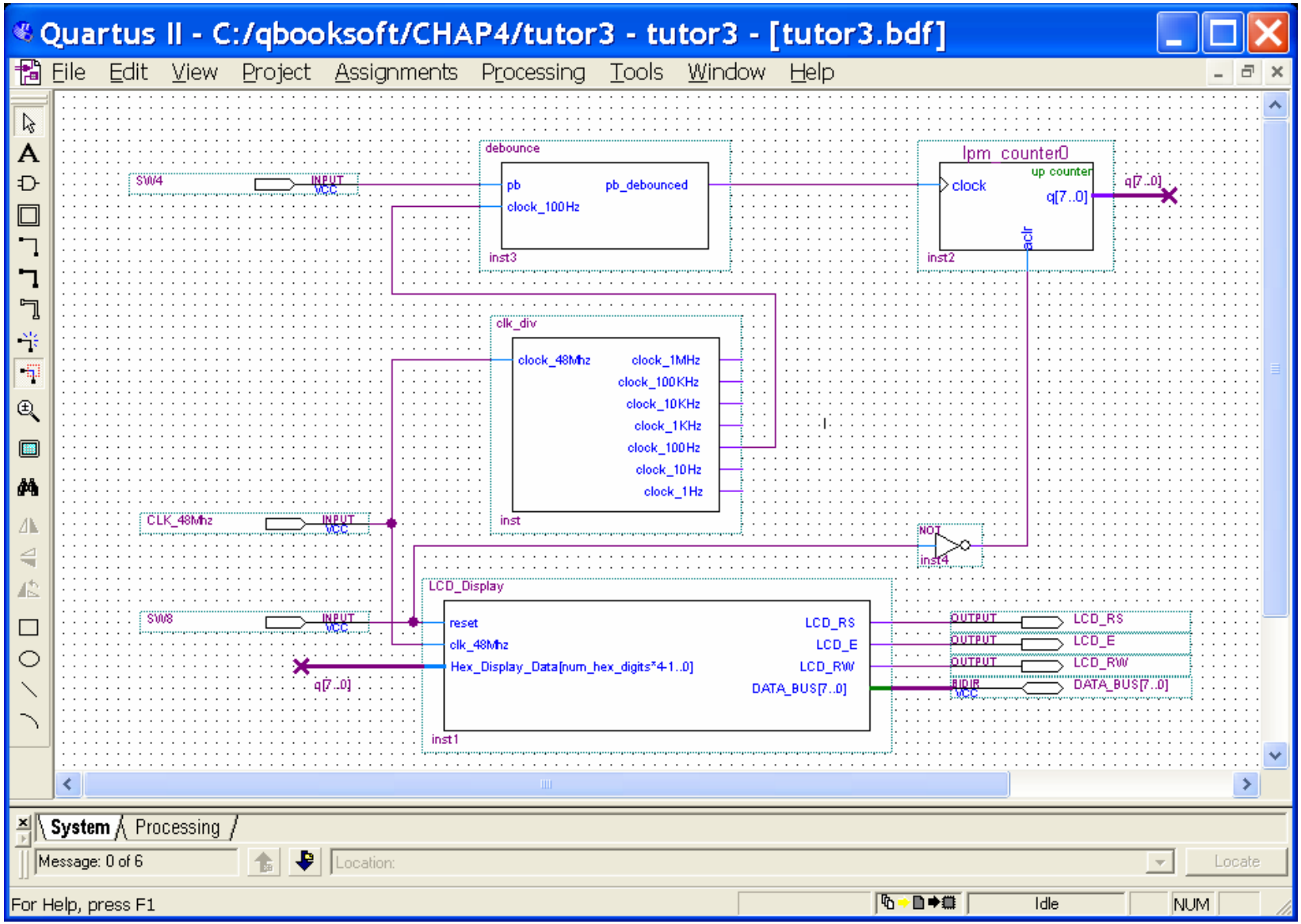

Figure 4.8 Modified tutor2 design schematic.